FIG.1

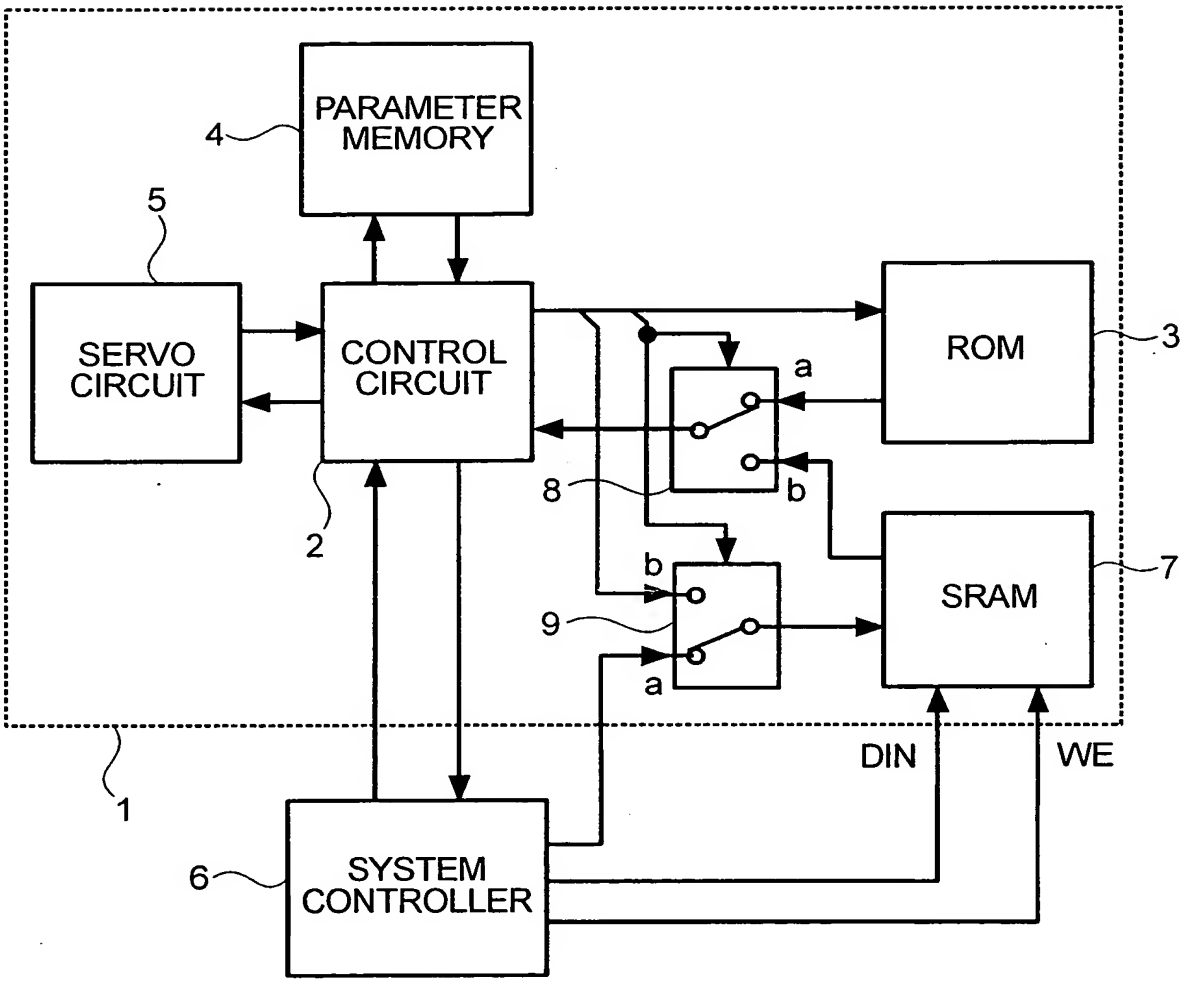
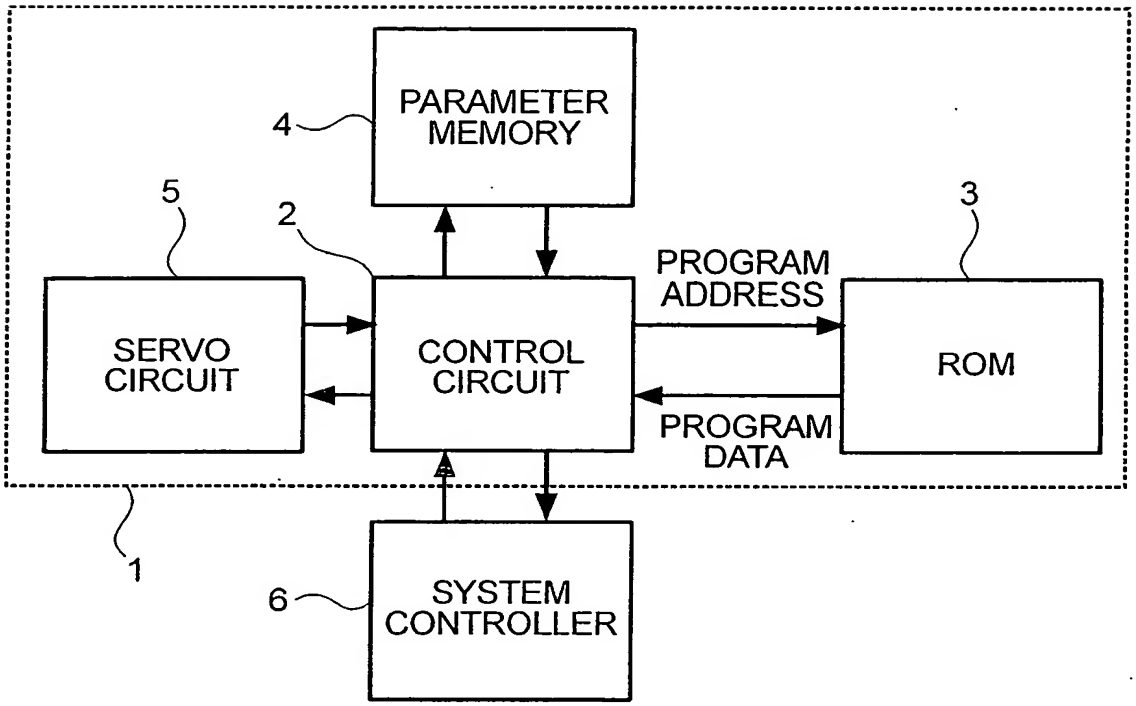

PARAMETER MEMORY
4
5
SERVO CIRCUIT
CONTROL CIRCUIT
2
ROM
3
a
b
8
b
9
a
SRAM
7
DIN
WE
1
6
SYSTEM CONTROLLER

FIG.2



(PRIOR ART)

FIG.3

DECIMAL ADDRESS	HEXADECIMAL ADDRESS	KIND OF PROGRAM DATA
0	0 0 0 0	ROM OUTPUT
1	0 0 0 1	
⋮	⋮	
8 1 9 0	1 F F E	
8 1 9 1	1 F F F	
8 1 9 2	2 0 0 0	SRAM OUTPUT
8 1 9 3	2 0 0 1	
⋮	⋮	
8 7 0 2	2 1 F E	
8 7 0 3	2 1 F F	

FIG.4

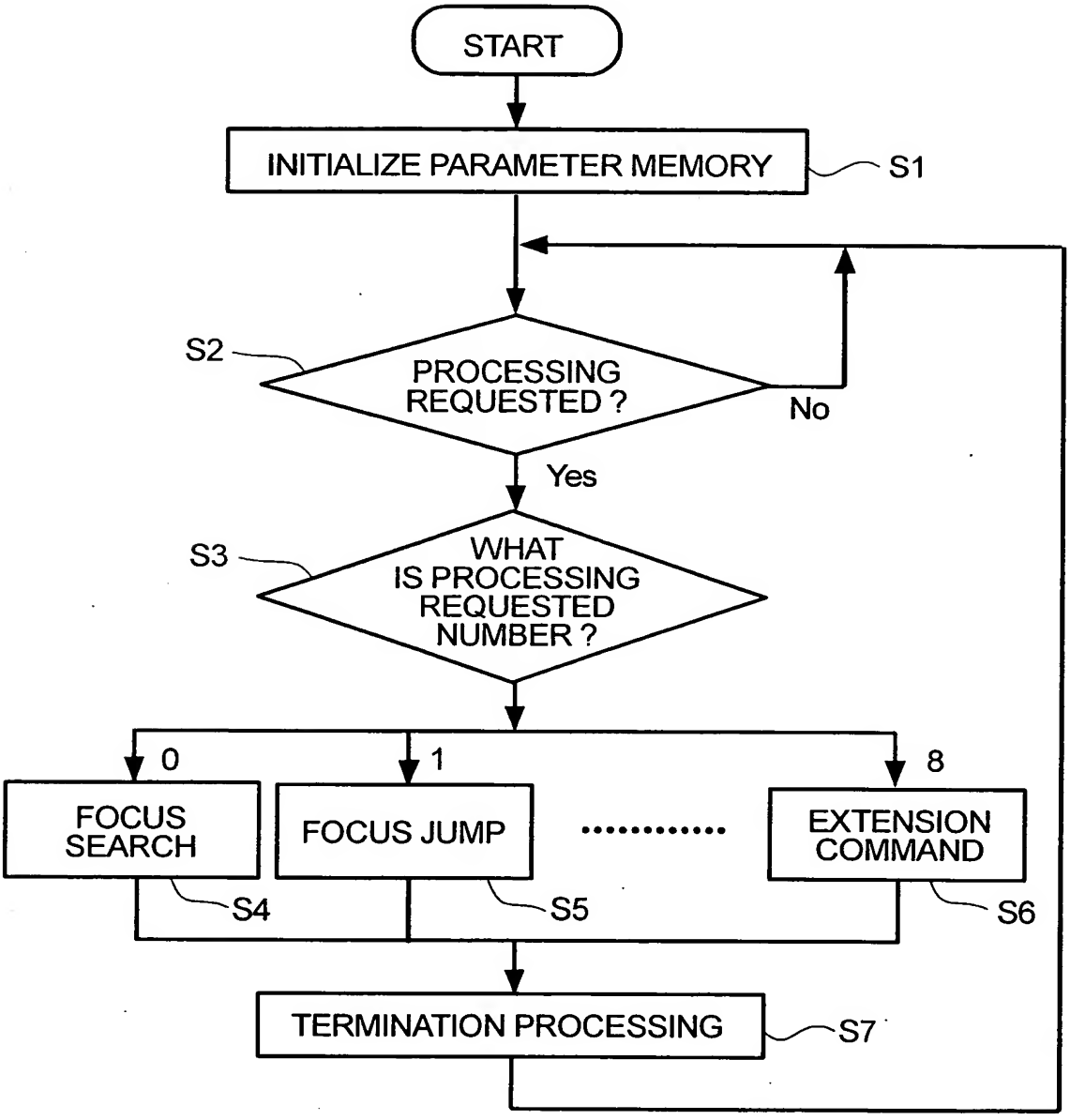

START
INITIALIZE PARAMETER MEMORY
S1
S2
PROCESSING REQUESTED ?
No
Yes
S3
WHAT IS PROCESSING REQUESTED NUMBER ?
0
1
8
FOCUS SEARCH
FOCUS JUMP
.....
EXTENSION COMMAND
S4
S5
S6
TERMINATION PROCESSING
S7

FIG.5

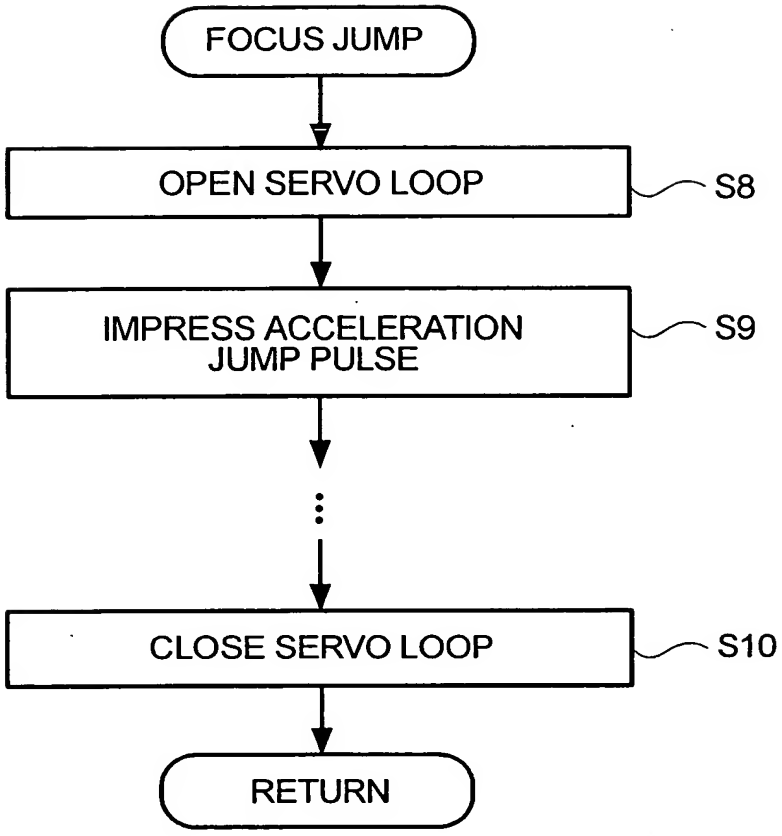

FOCUS JUMP
OPEN SERVO LOOP
S8
IMPRESS ACCELERATION JUMP PULSE
S9
CLOSE SERVO LOOP
S10
RETURN

FIG.6

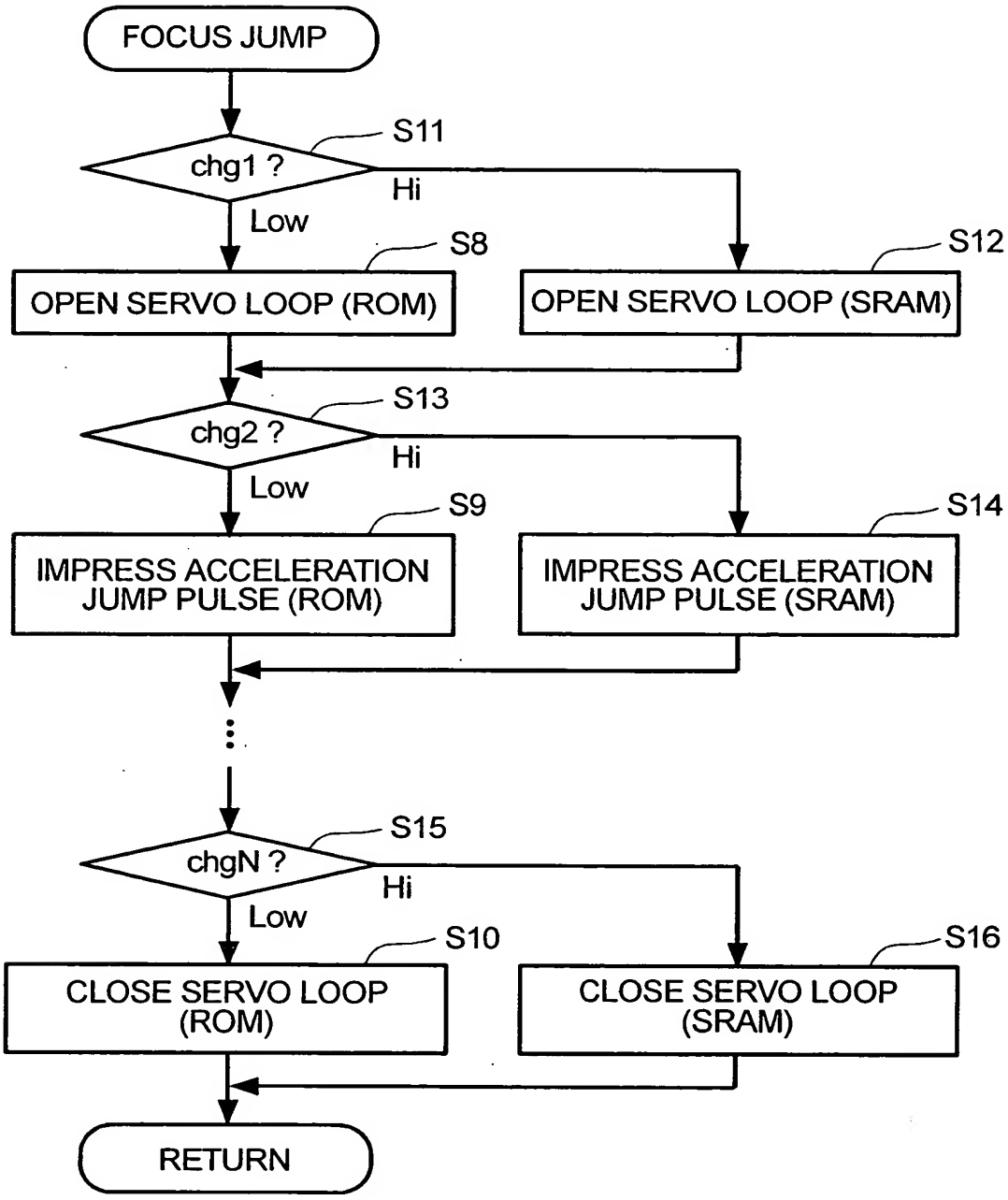
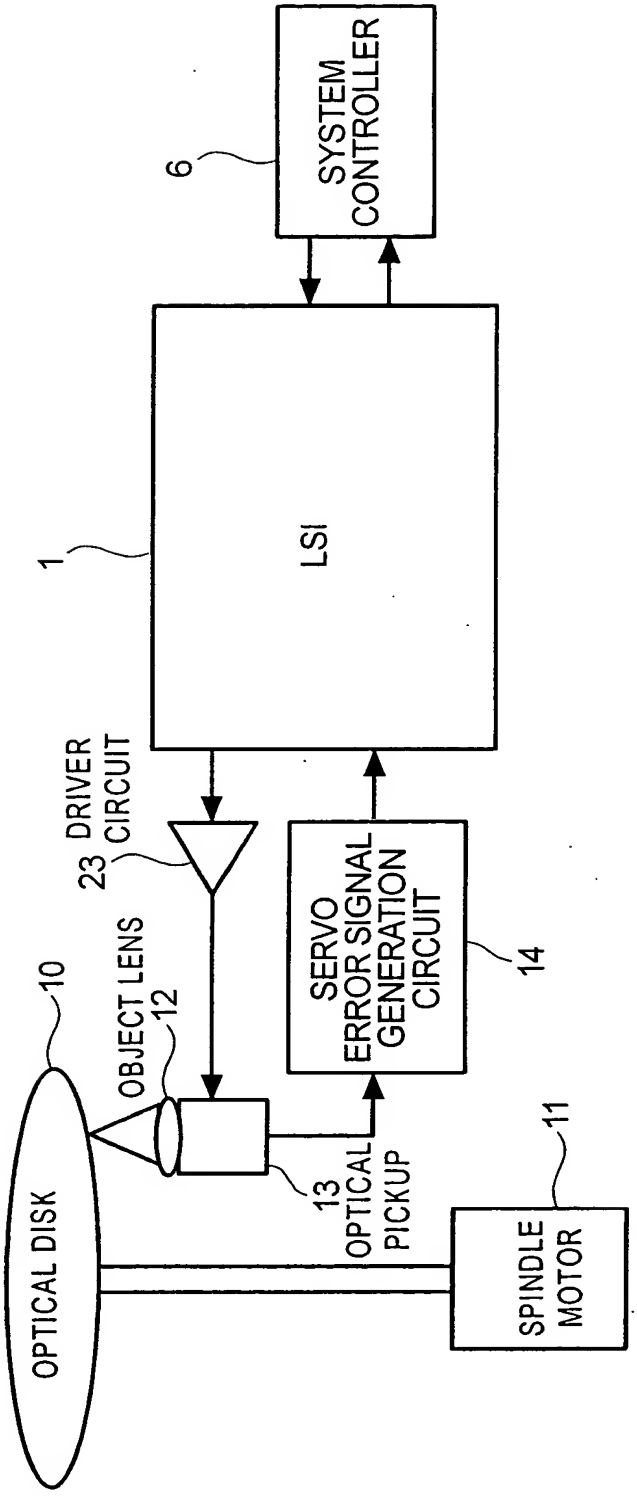

FOCUS JUMP
S11
chg1 ?
Hi
Low
S8
OPEN SERVO LOOP (ROM)
S12
OPEN SERVO LOOP (SRAM)
S13
chg2 ?
Hi
Low
S9
IMPRESS ACCELERATION JUMP PULSE (ROM)
S14
IMPRESS ACCELERATION JUMP PULSE (SRAM)
S15
chgN ?
Hi
Low
S10
CLOSE SERVO LOOP (ROM)
S16
CLOSE SERVO LOOP (SRAM)
RETURN

FIG.7


SYSTEM CONTROLLER
6
LSI
1
DRIVER CIRCUIT
23
SERVO ERROR SIGNAL GENERATION CIRCUIT
14
OBJECT LENS
12
10
OPTICAL DISK
13
OPTICAL PICKUP
SPINDLE MOTOR
11